

Fig. 10

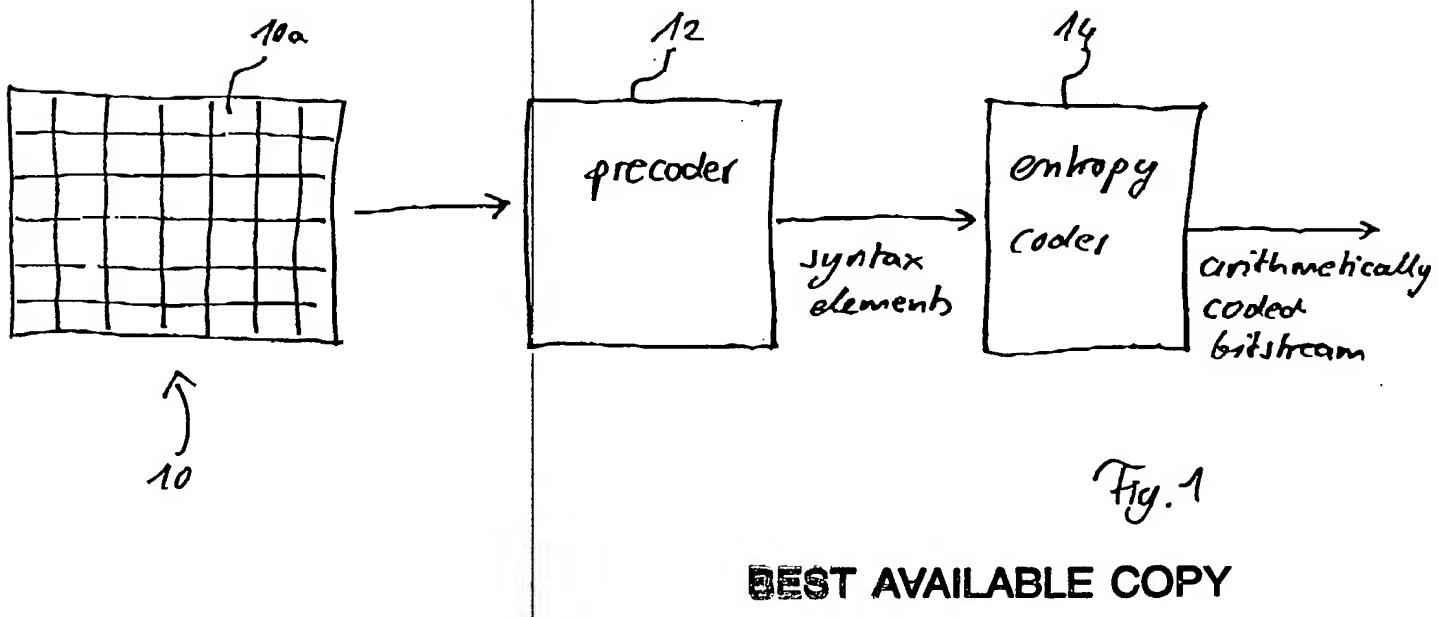
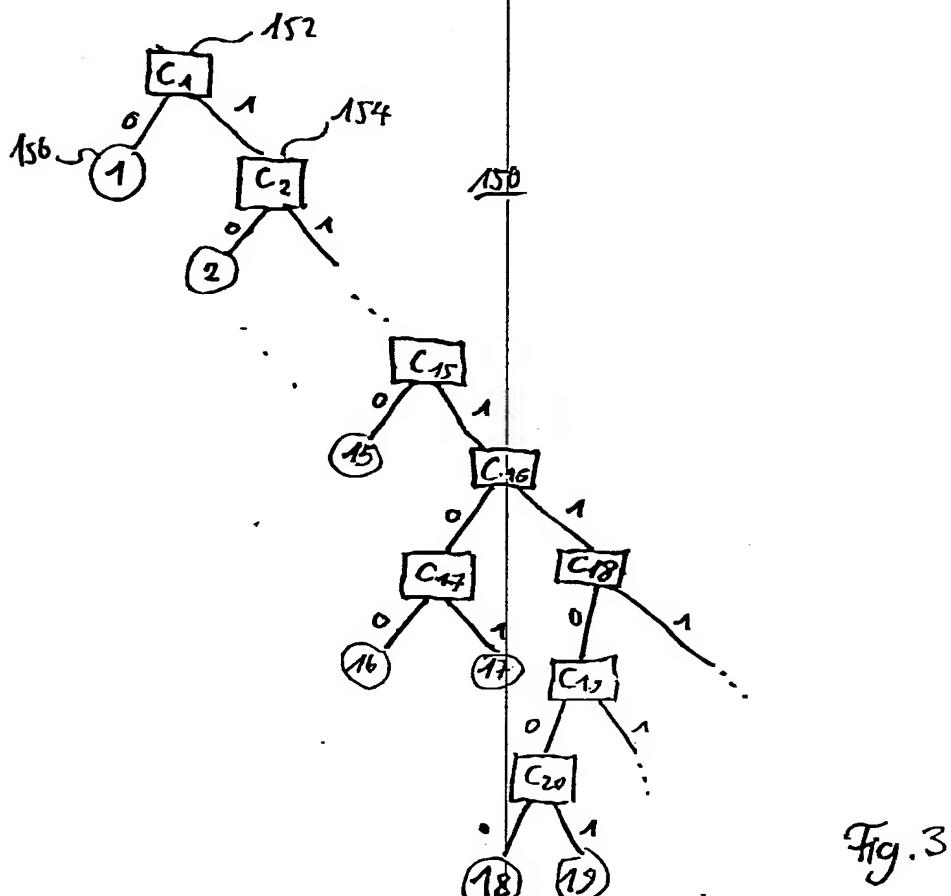
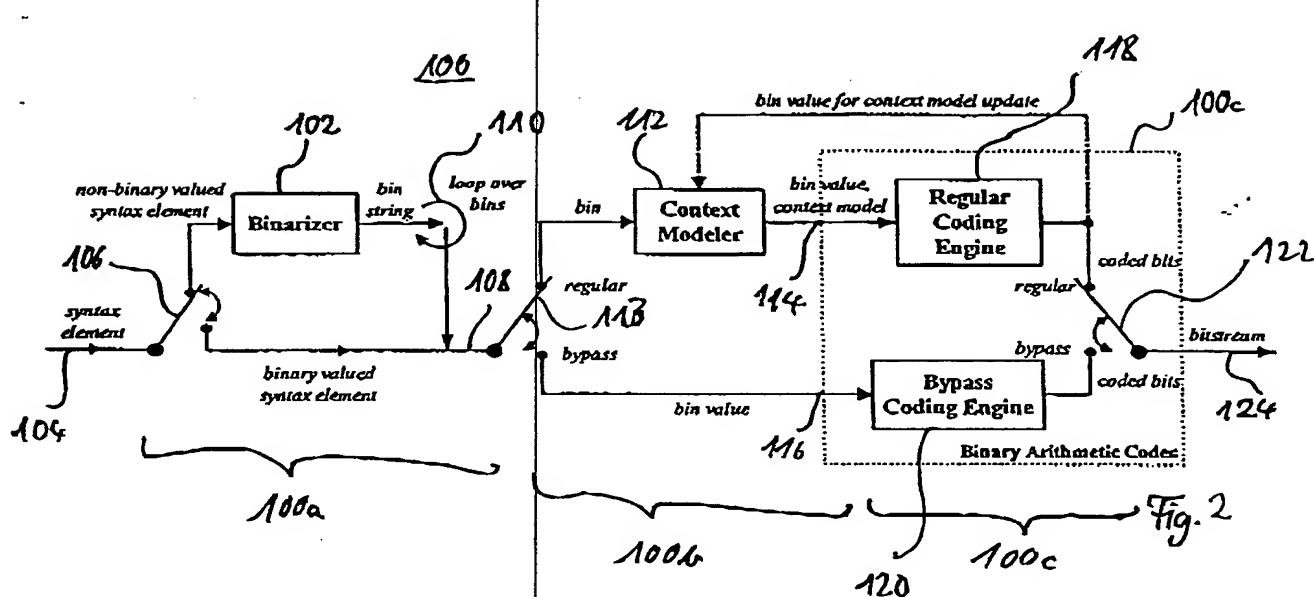


Fig. 1



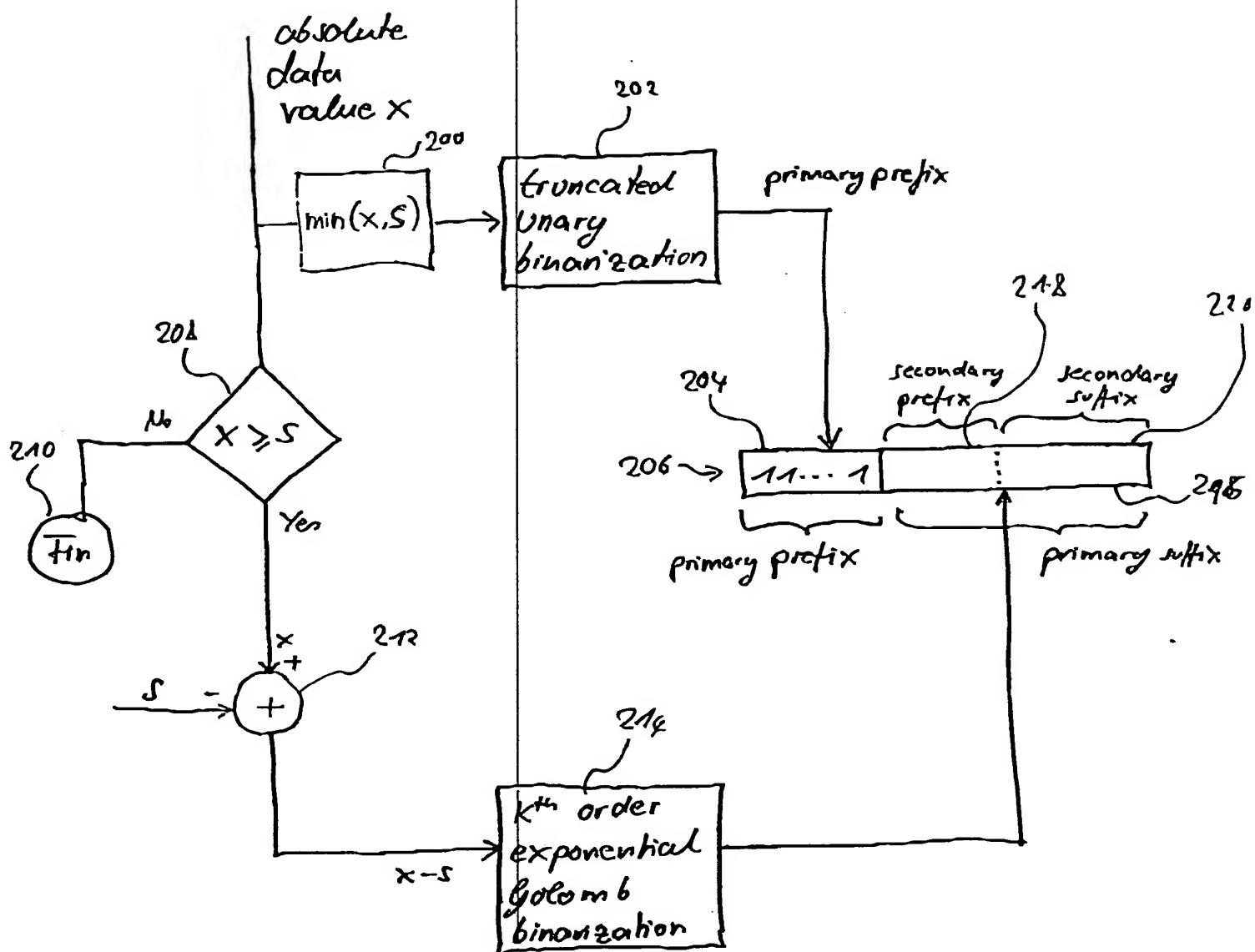


Fig. 4

Q	A	B	C	D	E	F	G	H
I	a	b	c	d				
J	e	f	g	h				
K	i	j	k	l				
L	m	n	o	p				

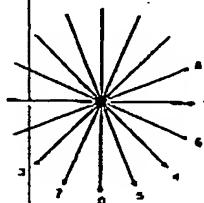


Fig. 15

Abs. value	Bin string													
	TU prefix										EG0 suffix			
1	0													
2	1	0												
3	1	1	0											
4	1	1	1	0										
5	1	1	1	1	0									
...
13	1	1	1	1	1	1	1	1	1	1	0			
14	1	1	1	1	1	1	1	1	1	1	1	0		
15	1	1	1	1	1	1	1	1	1	1	1	1	0	
16	1	1	1	1	1	1	1	1	1	1	1	1	0	0
17	1	1	1	1	1	1	1	1	1	1	1	1	0	1
18	1	1	1	1	1	1	1	1	1	1	1	1	1	0
19	1	1	1	1	1	1	1	1	1	1	1	1	1	0
20	1	1	1	1	1	1	1	1	1	1	1	1	1	0
...
bin	1	2	3	4	5	6	7	8	9	10	11	12	13	14
	15	16	17	18	19	...								

254

256

Fig. 5

252

250

258

Abs. value	Bin string	
	TU prefix	EG3 suffix
0	0 ← 270	
1	1 0	
2	1 1 0	
:	:	
7	1 1 1 1 1 1 1 0	
8	1 1 1 1 1 1 1 1 0	, 258
9	1 1 1 1 1 1 1 1 1 0 0 0 0	
10	1 1 1 1 1 1 1 1 1 1 0 0 1 0 1	
11	1 1 1 1 1 1 1 1 1 1 0 0 1 0	
:	:	
16	1 1 1 1 1 1 1 1 1 1 0 1 1 1	
17	1 1 1 1 1 1 1 1 1 1 1 0 0 0 0 0	
	254	256

Fig 6

```
300
302 k = 3
      if( Abs(mvd) >= 9 ) {
304      sufS = Abs(mvd) - 9
      stopLoop = 0
      do {
306          if( sufS >= ( 1 << k ) ) {
308              put( 1 ) ← 312a
310          sufS = sufS - ( 1 << k ) ← 312b
312          k++ ← 311c
314      } else {
316          put( 0 ) ← 312f
318          while( k-- ) ← 312g
320              put( ( sufS >> k ) & 0x01 ) ← 312h
322          stopLoop = 1 ← 312i
324      }
326  } while( !stopLoop ) ← 310
328
330  } if( mvd > 0 )
332      put( 0 )
334  else
336      put( 1 )
```

Fig.7

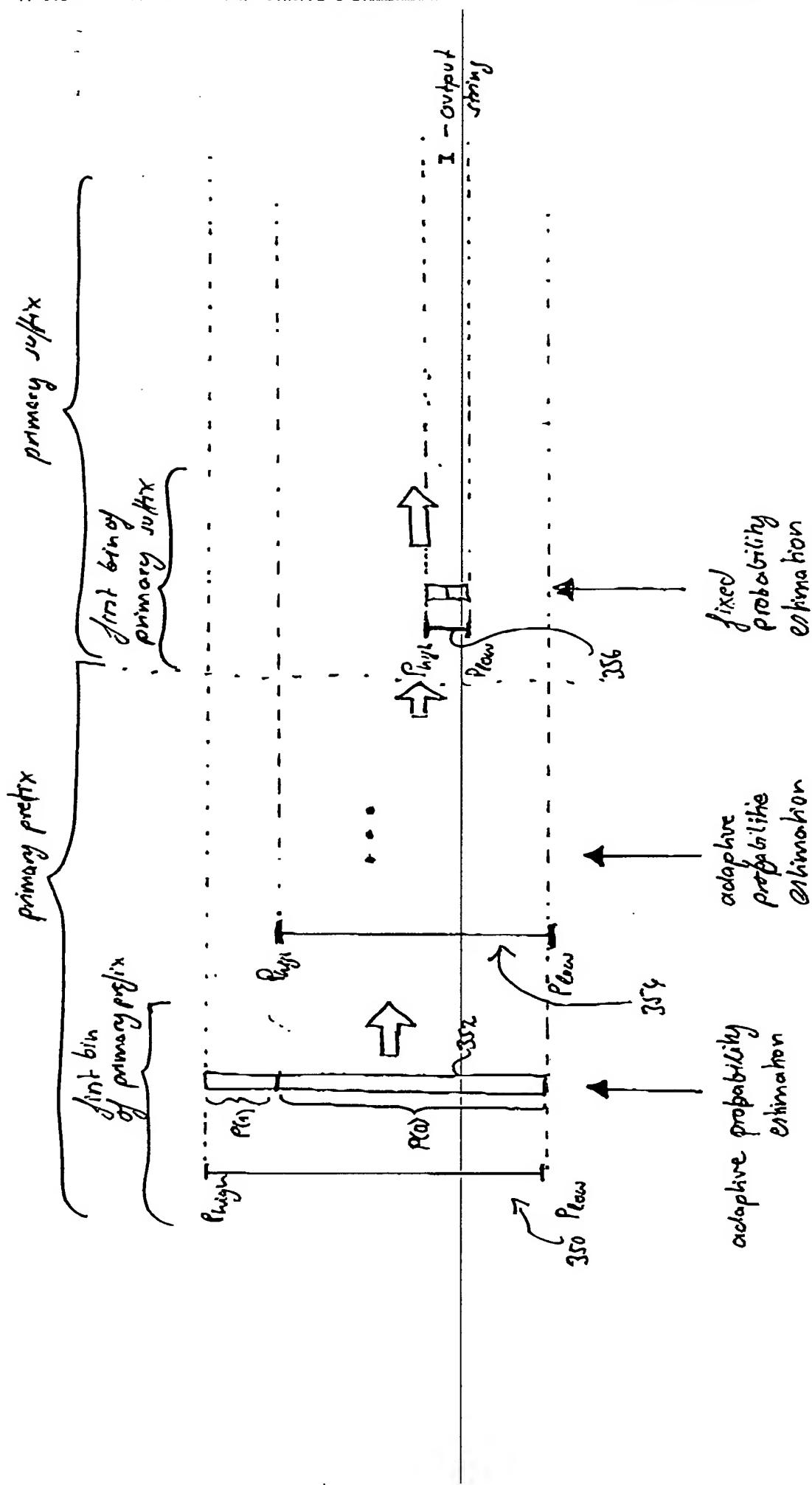


Fig. 8

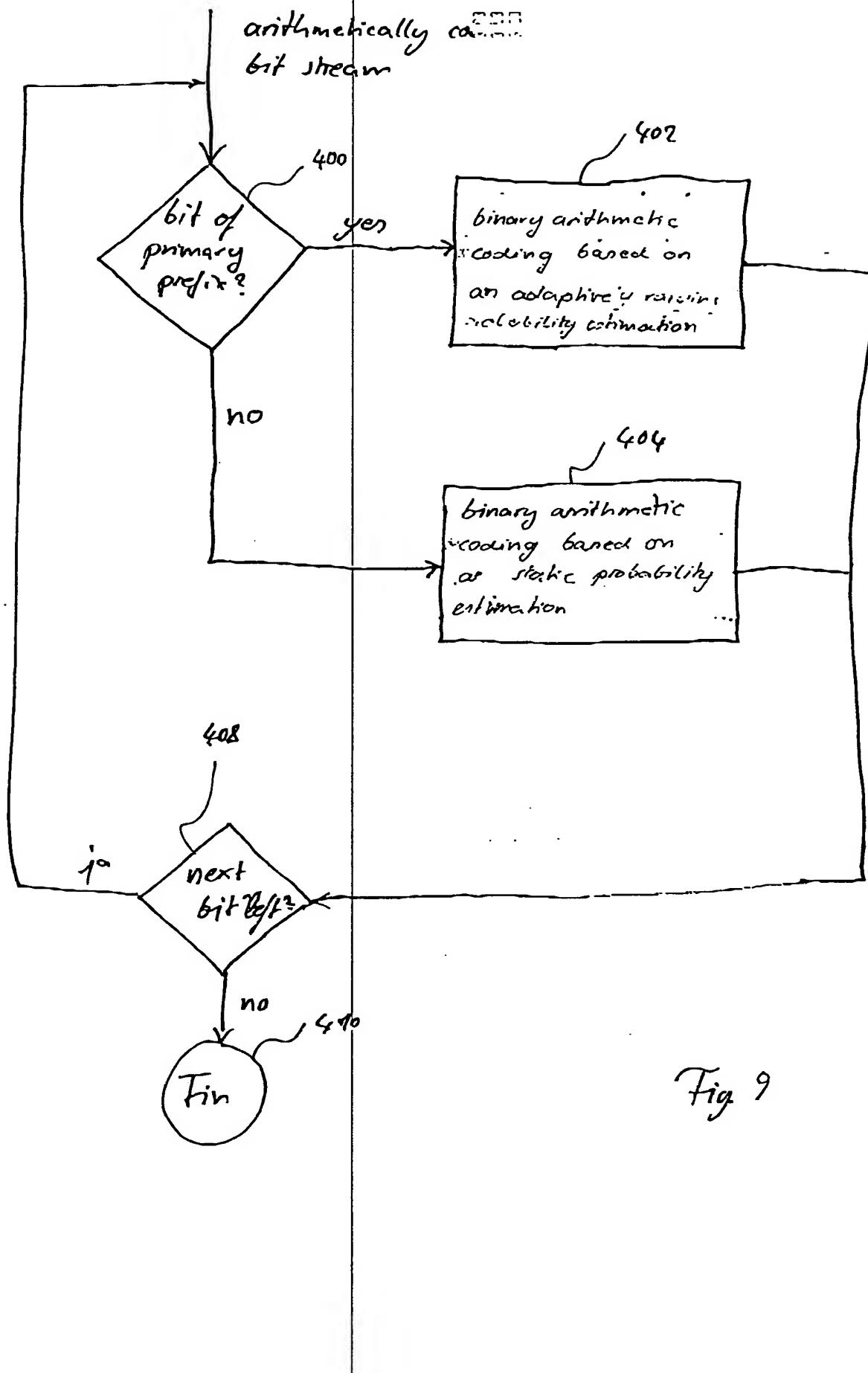


Fig. 9

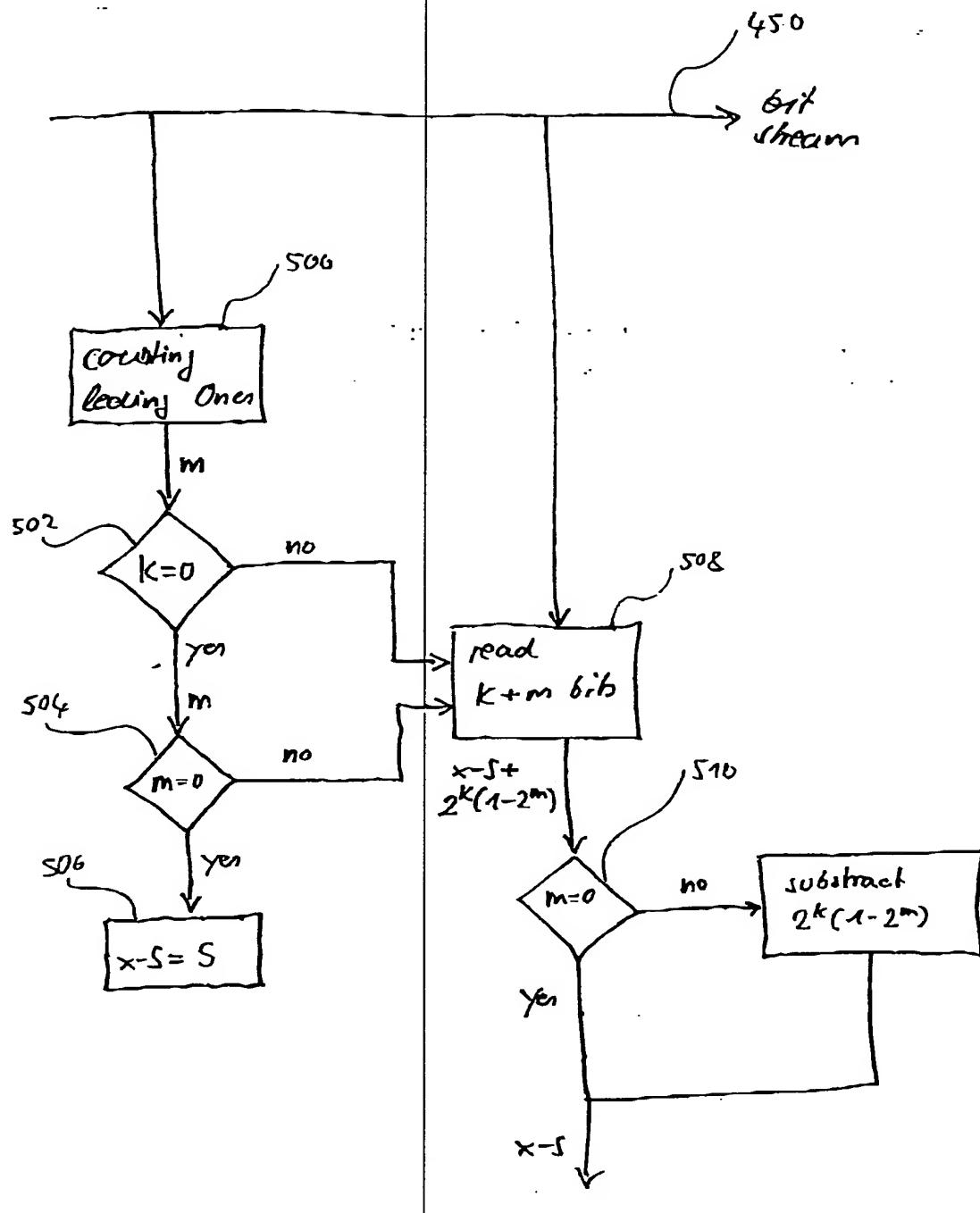


Fig. 11

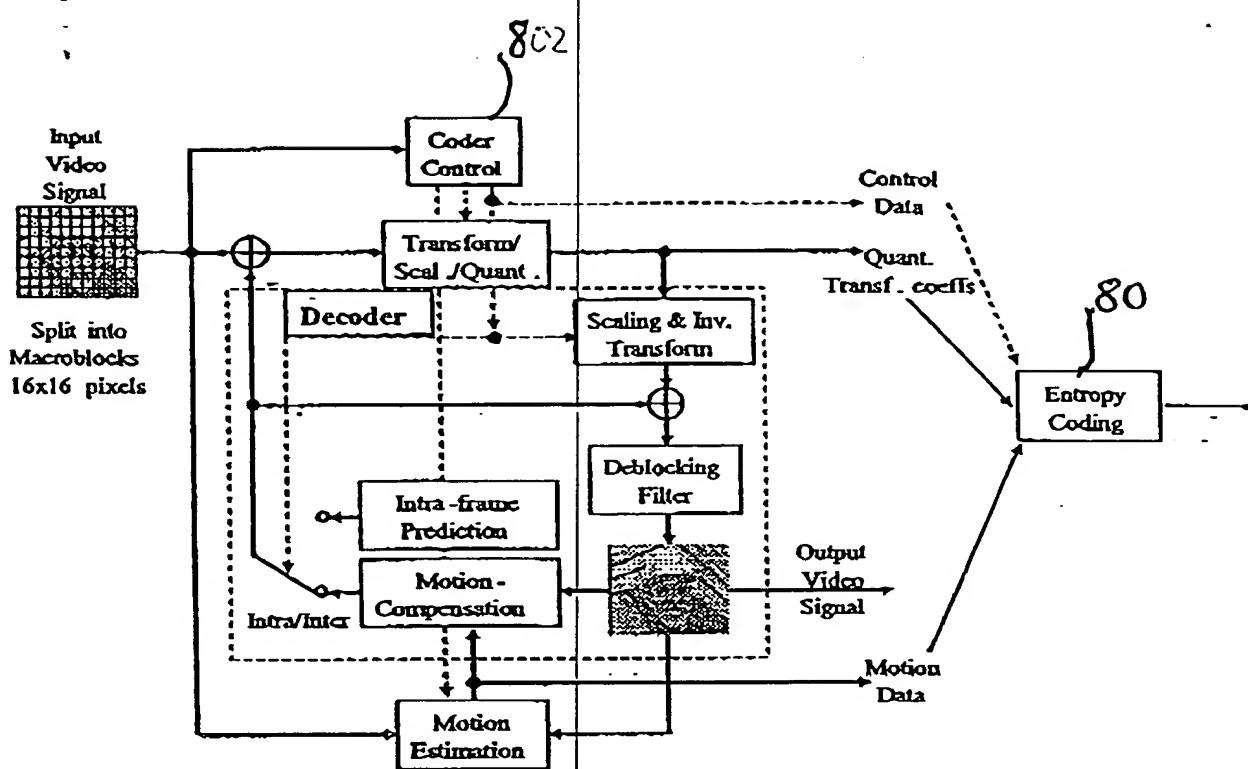


Fig. 12

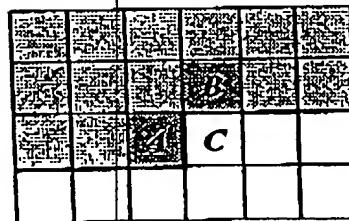


Fig. 93

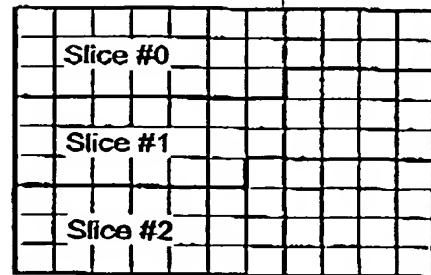


Fig. 14

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